

CLAIMS

What is claimed is:

1. A data transmission system, comprising:

2 a hub including a plurality of adapters including at least a requesting adapter
3 and at least a destination adapter;

4 a plurality of data processing systems, including at least a requesting data
5 processing system and at least a destination data processing system, coupled to said
6 hub via said requesting adapter and said destination adapter; and
7

8 a crossbar switch coupling said plurality of adapters, wherein said requesting
9 data processing system transmits at least a data frame to said destination adapter, said
10 requesting adapter converts said data frame into concatenated slots of an identical size
11 and transmits said data frame through said crossbar switch.

1 2. The data transmission system according to Claim 1, wherein said plurality of
2 data processing systems is a plurality of local area networks (LANs), including at
3 least a requesting LAN and at least a destination LAN.

1 3. The data transmission system according to Claim 1, wherein said plurality of
2 adapters is a plurality of local area network (LAN) adapters, including at least a
3 requesting LAN adapter and at least a destination LAN adapter.

1 4. The data transmission system according to Claim 1, wherein said data frame is
2 a LAN data frame.

1 5. The data transmission system according to Claim 1, wherein said crossbar
2 switch is an asynchronous transfer mode (ATM) crossbar switch.

1 6. The data transmission system according the Claim 1, said plurality of LAN
2 adapter further includes:

3 a control logic for transmitting a plurality of request signals (REQ) to said
4 crossbar switch, each of said plurality of requests signals associated with said
5 destination adapter.

1 7. The data transmission system according to Claim 1, said crossbar switch
2 further comprises:

3 a scheduler for transmitting a plurality of grant signals (GNT) associated with
4 said plurality of request signals enabling said requesting data processing system to
5 transmit at least said data frame to said destination data processing system, said
6 plurality of grant signals being transmitted in an order depending upon a
7 predetermined algorithm controlling said scheduler regardless the order said plurality
8 of requests are transmitted by said requesting adapter.

1 8. The data transmission system according to Claim 1, wherein said requesting
2 adapter further comprises:

3 a serial communication controller further including:

4 a means for converting said data frame received from said requesting
5 data processing system into serial data before transmitting said serial data to
6 said crossbar switch; and

7 a means for converting serial data before transmitting said data frame
8 to said destination data processing system.

1 9. The data transmission system according to Claim 8, wherein said serial data is
2 implemented as concatenated slots of a cell size in a high-level data link control
3 (HDLC) format.

1 10. The data transmission system according to Claim 8, wherein a plurality of
2 request signals (REQ) are serial encoded signals of thirty-two bits.

1 11. The data transmission system according to Claim 1, wherein each of said
2 plurality of adapters further includes:

3 a clock multiplier for multiplying by sixteen the data clock of the system and
4 providing said control logic with timing pulses used to transmit thirty-two bits of said
5 request signals.

1 12. The data transmission according to Claim 10, wherein a plurality of grant
2 signals (GNT) are serially encoded signals of thirty-two bits that are sampled by a
3 signal at the frequency of the data clock of the system multiplied by sixteen.

1 13. The data transmission system according to Claim 12, wherein said plurality of
2 request (REQ) signals and said plurality of grant (GNT) signals both include a first
3 pair of data bytes including sixteen bits defining a destination address of said data
4 frame to be transmitted and a second pair of data bytes including sixteen bits carrying
5 the connection time defined by the number of slots to be transmitted.

1 14. The data transmission system according to Claim 13, wherein said first data
2 byte defining said destination address includes one bit for each of said plurality of

3 adapters, a bit being set when said destination address corresponds to said destination
4 adapter, which enables a point-to-point connection, a multicast connection or a
5 broadcast connection.

1 15. The data transmission system according to Claim 11, wherein said serial
2 communication controller further comprises:

3 a means for generating a high-level data link control (HDLC) frame, in
4 response receiving said data frame from said requesting data processing system
5 coupled to said requesting adapter before transmitting said HDLC frames to said
6 crossbar switch.

1 16. The data transmission system according to Claim 15, wherein said means for
2 generating in said serial communication controller further includes:

3 a means for generating a high-level data link control (HDLC) flag to start said
4 HDLC frame;

5 a means for serializing a set of incoming parallel data bytes;

6 a means for computing a frame check sequence (FCS) after said set of
7 incoming parallel data bytes; and

8 a means for generating another HDLC flag to end said HDLC frame.

1 17. The data transmission system according to Claim 16, wherein said serial
2 communication controller further comprises:

3 a means for converting said high-level data link control (HDLC) frames
4 received from said crossbar switch into a plurality of data frames to be transmitted to
5 said destination data processing system.

1 18. The data transmission system according to Claim 17, wherein said means for
2 converting in said serial communication controller further includes:

3 a means for detecting a starting high-level data link control (HDLC) frame in a
4 incoming HDLC frame;

5 a means for checking the data integrity of said HDLC frame by computing a
6 frame check sequence (FCS); and

7 a means for deserializing the data bits of said HDLC frame to provide a set of
8 data bytes of said data frame.

1 19. The data transmission system according to Claim 18, wherein each of said
2 plurality of adapters further comprises:

3 a memory divided into at least two independent areas, a first data processing
4 system-to-switch area organized in a first plurality of buffers for storing at least said
5 data frame received from a data processing system coupled to said adapter and to be
6 transmitted to another data processing system, and a second switch-to-data processing
7 system area organized in a second plurality of buffers for storing at least said data
8 frame received from another data processing system and to be transmitted to said data
9 processing system coupled to said adapter.

1 20. The data transmission system according to Claim 19, wherein each of said
2 plurality of adapters further comprises:

3 a controller, for converting a serial data frame to a parallel data frame, or for
4 converting a parallel data frame to a serial data frame.

1 21. The data transmission system according to Claim 20, wherein said controller
2 comprises:

3 a means for synchronizing a clock during a set of preamble bytes when
4 receiving said data frame;

5 a means for detecting an incoming data frame through a delimiter byte of said
6 frame;

7 a means for checking data integrity of said data frame by computing a frame
8 check sequence (FCS) bytes;

9 a means for removing a set of protocol information of said data frame; and

10 a means for deserializing the remaining incoming bits of said data frame to
11 provide parallel data bytes.

1 22. The data transmission system according to Claim 20, wherein said
2 controller further includes:

3 a means for serializing incoming data bytes received from said serial
4 communication controller;

5 a means for generating protocol information bytes to be included in said data
6 frame; and

7 a means for computing said frame check sequence (FCS) of said data frame
8 before transmitting said data frame to a data processing system connected to an
9 adapter.

1 23. The data transmission system according to Claim 22, further comprises:

2 an arbiter for taking care of the contention between requests sent from said
3 controller and requests sent from said serial communication controller.

1 24. The data transmission system according to Claim 23, wherein said scheduler
2 further comprises:

3 an algorithm means for determining which request is to be granted each time a
4 new request (REQ) signal is received, said new REQ signal to be granted utilized as a
5 grant (GNT) signal being transmitted to said requesting LAN adapter; and

6 a control logic means, further including:

7 a memory for storing said request (REQ) signal received from said
8 requesting adapter.